

Abstract of the Disclosure:

In order to obtain a minimal leakage inductance in a semiconductor component, it is necessary to provide at least two adjacent switching elements whose load current connection 5 elements which are adjacent on one housing side to have different polarities. A multiplicity of even-numbered switching elements are advantageously disposed next to one another on an alignment line. The leads between the load current connection elements and the load current connections 10 of the switching elements that are disposed next to one another advantageously run approximately orthogonally with respect to the alignment line. The assigned load current connection elements then alternately have the first and the second supply potential and this minimizes the leakage 15 inductance.

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